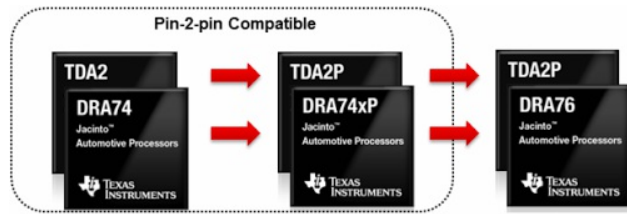


SOC PLATFORM

Image signal processor with CAN FD support

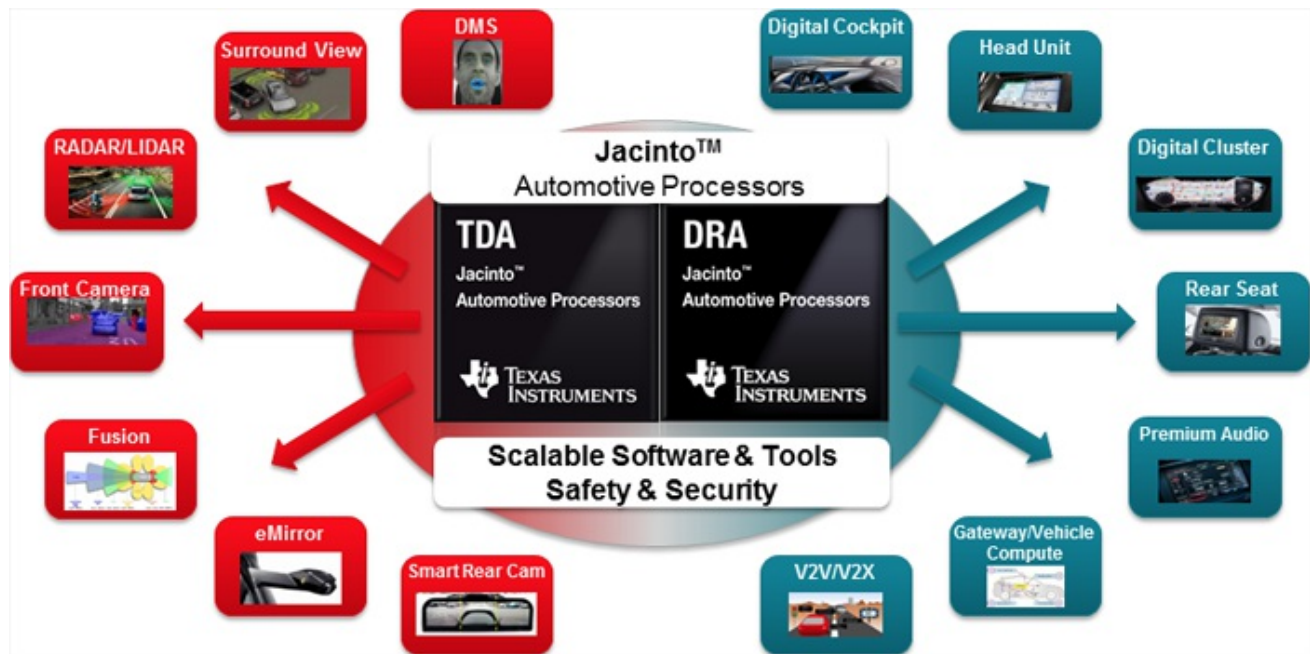
Texas Instruments (TI) has released the DRA76 and TDA2P system-on-chips (SoC). They are designed for automotive applications.



The scalable SoCs integrating an image signal processor come with CAN FD connectivity (Photo: Texas Instruments)

The TDA family of processors for ADAS (advanced driver assistance system) applications is part of the Jacinto automotive processor platform. It includes the DRA processors for digital cockpit, head unit, infotainment, and gateway ECUs (electronic control unit). The DRA76 and TDA2P processors feature CAN FD on-chip modules and a Camera Serial Interface (CSI-2). The chips provide software compatibility with the Jacinto predecessor products. The TDA2P is dedicated for cost-sensitive camera monitoring systems including 3-D surround systems. The DRA76 supports displays with resolutions up

to 2880 by 1080 pixels. Software development kits (SDK) are available. Recently, Texas Instruments has launched the TDA3 processor designed for entry-to-mid performance ADAS applications. This product provides optionally CAN FD connectivity.



The Jacinto automotive processors support a broad range of automotive applications (Photo: Texas Instruments)

TI has shipped more than 150 million of its ADAS and digital cockpit SoCs to more than 35 OEMs. The Jacinto family is software and hardware compatible with ARM's Cortex-A15 cores. It powers infotainment systems such as Ford's Sync-3, Volkswagen's MIB-II, SAIC-Alibaba's Internet-Car, and solutions by BMW.

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