

CANopen support for Cortex MCUs

Port (Germany) has extended its CANopen protocol stack offering in the area of ST Microelectronics' (France/Italy) Cortex micro-controllers. The software runs on the STM32F1xx, STM32F2xx, and STM32F4xx 32-bit MCUs.

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Core/features

[Zoom](#)

THE SCALABLE CANOPEN LIBRARY complies with the CiA 301 communication profile (version 4.2) and the EN 50325-4 standard (CiA 301 version 4.0). It supports all CANopen services and protocols. The software is written in ANSI-C. The longtime CiA member company provides dedicated libraries for general CANopen NMT devices as well as for simple devices needing just limited CANopen functionality. The NMT master/slave library supports Heartbeat as well as Node/life-guarding. Of course, the library features the complete NMT slave functionality as well. The low-level driver software for the Cortex components is suitable for multi-thread and single-thread implementations. It can be configured for BasicCAN and FullCAN functionality. "Using our CANopen Design Tool (CDT) reduces development and implementation effort," said Dietmar Franke, CEO of Port. For most of the MCU versions CANopen boot-loaders are available.