


## Micro CANopen Plus

 Micro CANopen Plus is a small-footprint, commercial-grade CANopen implementation with advanced features based on flexible CANopen compliant source code. Designed for small footprint applications. Ideal for situations requiring medium configurability during run-time and great performance on any type of platform, and for building networks that include manager nodes, Micro CANopen Plus provides the most flexible solution. Auto-generated configuration from the EDS/DCF file makes its setup a quick and painless one-step process.

**Portable.** Micro CANopen Plus is written in 100% standard ANSI C code allowing for straightforward porting. For targets without standard support packages, designers familiar with their target processor can easily perform the port themselves. Or, ask us for a quote to add support to your target!

**Compact.** ROM Specifications: 7K - 14K bytes (for CANopen Slaves) depending upon processor and options selected.

**An Affordable Pricing Structure.** Micro CANopen Plus offers a low, one-time fee and no royalties on deployed products and includes all source code with every purchase.

### Feature Highlights

The following is a non-exhaustive overview of features in Micro CANopen Plus:

- NMT State Machine
- Object Dictionary with Process Image
- Expedited, segmented and block-transfer SDO
- Multiple SDO servers
- SDO fully-meshed (matrix) communication setup with up to 16 nodes when used together with the Manager Add-on
- PDO with runtime-configurable event time, inhibit timer and transmission type
- SYNC PDO, both receive and transmit
- Heartbeat Generator
- Heartbeat Consumers
- Emergency Generator
- Node Guarding (select platforms)
- Non-volatile save/restore of parameters
- Code configured from EDS or DCF file with [CANopenArchitectEDS](#)
- Extensive call-back API interface
- Memory access hooks for use of the stack in RTOS-based applications

### What's Included

Micro CANopen Plus is delivered with an example CiA401 (Generic I/O) implementation and drivers for NXP ARM and CANopen Magic Ultimate simulation.

Customized examples for other device profiles are available under our consulting services. Our stack passes the official CANopen Conformance Test!

Full documentation is supplied. **All software products include a one-year maintenance and priority support agreement that can be extended anytime.**

Also included is a single licence for [CANopenArchitectEDS](#), an EDS/DCF editor with code generation for quick stack configuration.

### Currently Available Ports

The following list is an example of the microcontrollers and compiler combinations that Micro CANopen Plus is currently available for as optional add-ons. More combinations are available, please contact us with your requirements. For currently unsupported combinations you can either perform the port yourself or we can perform it for you.

- NXP LPC ARM7, Cortex-Mx families w/ Realview, LPCpresso, GNU
- ST STM32 family w/ Realview
- TI Luminary LM3S family w/ Realview
- Microchip PIC18 family w/ MPLAB and C18
- Atmel AVR w/ WinAVR, ImageCraft
- Atmel AT89C51CC0x w/ Keil
- Freescale S08 family w/ CodeWarrior
- Freescale S12 and S12X families w/ CodeWarrior
- TI TMS470 family w/ Realview
- Renesas R8C23 and M16C w/ HEW (High-performance Embedded Workshop)
- Peak System PCAN PC CAN interfaces w/ MS Visual C++
- PC Simulation (with [CANopen Magic Ultimate](#))

## Contact

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## Features

No features listed.

